Two Improved Methods for Testing ADC Parametric Faults by Digital Input Signals

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I. INTRODUCTION

NOWADAYS, mixed-signal testing becomes a challenge in production test. More and more analogue-to-digital converters (ADC) are integrated into platform-based designs. These designs are usually for video, audio and high-speed communication systems. As the systems develop, the speed and resolution of the ADCs in the platform designs become higher accordingly. As a result, higher quality analogue signal sources are required in the conventional production test, which leads to an increase of the test cost. [1] One of the most efficient ways to reduce the production test cost is multi-site testing, which can test a number of devices under test (DUT) in parallel. However, increasing the number of ADCs under test usually requires more high-quality analogue signal sources, which are very expensive [2]. In this case, it weakens the advantage of multi-site testing. Compared to an analogue signal, a digital signal is much easier and inexpensive to generate in a platform-based design, which is composed of memories, RF and mixed-signal front ends and importantly the multiple-processor cores in general. In [3], an adapted digital waveform is explored to test an ADC. However, the spectrum of a pulse wave is distorted with harmonics related to the pulse rise and fall times and duty cycle, making accurate determination of ADC parametric faults complex and time consuming [3]. In this paper, we propose two methods to improve the results in [3]. One method explores using a multiple-level digital stimulus to test the ADC. The other method applies an improved post-processing method. Both of the methods are expected to be a quick pre-test for discarding the faulty devices by a signature result. After that, only the fault-free devices are tested by the complex conventional test. In this way, test time and cost can be saved.

In the past years, some published works have dealt with decreasing the cost and requirement of the accurate analogue input stimulus for ADC testing. In the work [4-6], the authors used two low-resolution ramp signals with offset to test the high-resolution ADC. By applying a stimulus error identification and removal algorithm (SEIR), a 16-bit ADC can be accurately tested by a 7-bit ramp signal. The authors in [7] generated a stair-case signal by a pulse-width modulation (PWM) signal and an off-chip low-pass filter. As the stair-case signal is taken as the test stimulus, the 3rd harmonic distortion of an ADC up to twenty bits can be tested with a 3rd order polynomial fitting algorithm. The test accuracy is primarily limited by the linearity of the low-pass filter. The reference [8] presents a work that using a fully binary PWM input signal simultaneously tests the gain, offset, 2nd and 3rd harmonics and signal-to-noise-ratio of an ADC. It is only suitable for ∑Σ ADCs. A white noise is employed as the test stimulus in [9]. In the spectrum of the white noise, all the frequencies are expected to equally appear. As the non-linearity of ADCs will introduce some extra frequencies, the non-linear faults of ADCs can be detected by analyzing the extra frequencies at the ADC output.

II. ADC TESTING USING A MULTI-LEVEL PULSE WAVE INPUT SIGNAL

An adapted pulse wave can be expressed in the time domain as:

\[ x(t) = \frac{A}{T_r}(u(t) - u(t - T_r)) + A(u(t - T_r) - u(t - (T_r + T_h))) + (1 + \frac{T_h}{T_f})u(t - (T_r + T_h)) - u(t - T_r + T_h + T_f) \]  

where \( A, T_r, T_f \) and \( T_h \) denote the amplitude, rise and fall time of the signal and time of high-level, respectively. According to the Poisson formula, discrete time derivative is applied to the time-domain function in (1). As a result, the spectrum of the adapted pulse wave can be found as:

\[ F[x(t)] = \frac{A}{\omega^2 T_r} (e^{-\omega T_r} - 1) - \frac{A}{\omega^2 T_f} (e^{-\omega T_f} - 1) - \frac{2jA}{\omega^2 T_r} \sin \frac{T_r}{2} e^{-\frac{\omega T_r}{2}} + \frac{2jA}{\omega^2 T_f} \sin \frac{T_f}{2} e^{-\frac{\omega T_f}{2}} \]  

Spectral representation of the adapted pulse wave in (2) shows that compared to the ideal spectrum of a pure sine wave only containing the fundamental frequency, it also contains other harmonics depending on the rise and fall times and the duty cycle. In [3], an ADC is evaluated through the FFT analysis, using an adapted pulse wave as the test stimulus. The rise or fall time of the pulse wave stimulus should be set properly to hit all the output codes of the ADC under test. Based on the requirement of Nyquist limits and the principle of coherent sampling, the rise/fall time should be at least larger than the reciprocal of the sampling frequency of the ADC under test 1/fs. The approximate values of the dynamic parameters of the ADC can be obtained. In the simulation, to obtain the fault-free range, Monte Carlo simulations are carried on the fault-free ADC by the pulse wave stimulus. As a result, a decision of pass or fail is made by the approximate values to distinguish the faulty devices from the fault-free devices. In real test, a number of golden
devices, which are fault-free devices tested by the conventional method, are tested by the pulse wave to obtain the fault-free range. The number of the golden device should be sufficiently large to cover the process variations. However, the results obtained in [3] can not fully cover all types of parametric faults. It is caused by the difference of the spectrum between sine and pulse waves.

One of the potential methods to improve the result is approximating to the spectrum of a sine wave by adjusting the shape of a digital input stimulus. A sine wave can be considered as a stair-case pulse wave with countless voltage levels. The more voltage levels in a stair-case pulse wave, the more the spectrum of the pulse wave will be similar to the one of a sine wave. Nowadays, most of the ADCs are integrated into a platform-based design. In general, there are multiple power supply levels (usually three levels in maximum) in a platform design. In this case, it provides the potential to generate a pulse wave with different levels. Compare with generating an accurate analogue sine wave on-chip, a stair-case pulse wave is easier and less expensive to generate in such a kind platform-based design. As a starting point, the stair-case pulse wave with multi-level in Fig.1. is applied to both golden devices and the DUTs as the test stimulus. The output spectra can be obtained respectively via FFT analysis. Compared to a single-level pulse wave input stimulus, the output spectrum of the multi-level pulse wave is more close to the sine wave. In this case, one can expect that the output spectrum from a multi-level pulse wave test stimulus can detect the parametric faults in a more sensitive way as compared to its binary counterpart.

In [7], a stair-case waveform is also applied for testing the 3rd harmonic distortion of an ADC. The method is based on the polynomial fitting technique and requires high linearity of the rise/ fall slopes of the stair-case waveform.

In [3], we only use the conventional way to evaluate the output spectrum, like calculating the total harmonic distortion (THD). However, the complexity of the spectrum of a pulse wave and the nonlinearity of the ADC makes the variation of the output spectrum very different from the one obtained from a sine wave stimulus. In the second method, we focus on the evaluation method of the test output and still keep using a pulse wave as the test input signal. Instead of calculating the conventional dynamic parameters, we only calculate the similarity of the output spectrums between the golden devices and the DUTs. As shown in Fig. 2, a single-level pulse is applied to both golden devices and the DUTs as the test stimulus. After the FFT analysis, the output spectra of golden devices and the DUTs can be obtained respectively. After that, a correlation algorithm is used to calculate the similarity between the output spectrums of the golden devices and the DUTs. If the result shows their spectra are not sufficiently similar, the device is considered to be a faulty device. Otherwise, it is fault-free. In [10], a specific correlation is used to evaluate the output results obtained from a pulse wave as well. However, the correlation is applied in the time domain of the output waveform while we apply the correlation in the frequency domain of the output in this paper. The out-of-range-percentage (OFRP) is used to distinguish faulty devices from fault-free devices.

In this work, we only focus on the parametric faults in the analogue part of the ADC. As shown in Fig. 3, the original input is applied to the first stage. The following stages will amplify and divide the input into several resolution levels through the resistor ladders. In the meanwhile, any fault in the first stage will be amplified by the following stages too. As a result, the ADC is most sensitive to the faults in the first
stage. To evaluate the improved test methods, parametric faults are injected into the first stage which is composed of 11 pre-amplifiers. The amplifier is randomly chosen to inject three types of faults:

1) offset fault – insert a dc voltage source into the gate of the one of the input pair transistors
2) gain fault – change the resistance of the load resistors of the amplifier
3) bandwidth fault – insert an extra capacitor at the output of the amplifier

With the gain and bandwidth fault, the sine wave input stimulus can detect the faults much better than the pulse wave. However, with the offset faults, the multi-level pulse wave can detect the faults even better than the sine wave input signal.

Fig. 4. The input pulse waves with different levels

V. SIMULATION RESULTS

A. ADC testing using multiple-level pulse wave input signal

Three pulse waves with a different number of voltage levels are applied to the ADC respectively. As shown in Fig. 4, all the pulse waves have the same the frequency $f_c=7$ MHz, the total rise/fall time $T_r/T_f = 30$ ns, the lowest voltage level $V_{low} = -0.46$ V and the highest voltage level $V_{high} = 0.46$ V. The sampling frequency of the ADC is 300 MHz.

The results of the THD values with offset faults of the first-stage pre-amplifier are shown in Fig. 5. The x-axis indicates the ratio between the faulty offset and the fault-free offset, while the y-axis denotes the THD values of the ADC output obtained by the input pulse waves. The three curves in different colours represent the results by those three different types of pulse waves respectively. One can see that as the number of voltage levels increases the change of the THD value by the offset faults is more obvious. It means that the THD results can reflect the faults in a more sensitive way.

After running the simulation with all the three types of faults respectively, the results by different types of input stimuli are shown in Table 1. In each type of fault, 8 different faulty cases are injected in total. The number of faulty cases, which are detected by the input stimulus, is listed in Table 1. It shows that with the gain and bandwidth faults, the sine wave input stimulus can detect the faults better than all the other pulse waves input stimuli. However, with the offset faults, the 3-level pulse wave input signal can detect the fault as well as the sine wave input signal. The 4-level pulse wave is even more sensitive to the offset fault than the sine wave. It can detect 6 faulty cases while the sine wave can only detect 5 faulty cases.

With the gain and bandwidth fault, the sine wave input stimulus can detect the faults much better than the pulse wave. However, with the offset faults, the multi-level pulse wave can detect the faults even better than the sine wave input signal.

Fig. 5. The results of the THD by pulse waves input signals

<table>
<thead>
<tr>
<th>TABLE I</th>
<th>THE NUMBER OF DETECTED FAULTY CASES BY DIFFERENT TYPES OF INPUT STIMULI</th>
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<tbody>
<tr>
<td>Input stimulus</td>
<td>2-level pulse wave</td>
</tr>
<tr>
<td>Type of faults</td>
<td></td>
</tr>
<tr>
<td>Offset faults</td>
<td>0</td>
</tr>
<tr>
<td>Gain faults</td>
<td>0</td>
</tr>
<tr>
<td>Bandwidth faults</td>
<td>0</td>
</tr>
</tbody>
</table>

B. ADC testing by comparing the deviation of the output spectrum

In practice, the input pulse wave has the jitter and variation of the rise/fall time. In order to see these effects to the output
results, three different types of input stimuli are applied to the ADC:

Ideal pulse wave: \( V_{\text{high}} = 0.46V, V_{\text{low}} = -0.46V, T_r/T_f = 10\text{ns}, f_{in} = 7\text{MHz} \)

Pulse wave with jitter: add a random jitter into the ideal pulse wave, spectral frequency density = \(2.67 \times 10^{-3}\), bandwidth = 3.1GHz.

Pulse wave with jitter and rise/fall time change: make the rise/fall time 0.1 ns different from the pulse wave with jitter.

With the same faulty cases, the range of the deviation of the OFRP is almost 4 times in Fig. 7 larger than the one in Fig. 6.

In this case, with the offset faults, the OFRP tested by comparing the output spectrum is less sensitive to the jitter and rise/fall edge variation of the input pulse wave compared with the OFRP tested by comparing the output amplitude.

For the other two types of faults, the results of the OFRP are similar to the ones with offset faults that are less affected by the jitter and rise/fall edge variation when comparing the output spectrum.

In this method, a signature result can be obtained by the parameter OFRP. With all the three types of faults, it is more sensitive to the faults and less sensitive to the jitter and rise/fall time change than the previous method [10].

VI. CONCLUSION

In this paper, two improved methods are presented extending our previous work. The first one improves the results by adjusting the voltage levels of the input pulse wave stimulus. Compared with the sine wave input stimulus, the four-level pulse wave can detect even more faulty cases with the offset faults. The second one improves the results by calculating the similarity of the output spectra between the golden devices and the DUTs. Compared with the previous method [10], it is less sensitive to the jitter and the change of the rise/fall time of the input pulse wave stimulus. In these two methods, a number of golden devices are tested at first to obtain the fault-free range. At last, a signature result is obtained from both methods. It can filter out the faulty devices in a quick way before testing the specific values of the conventional dynamic and static parameters.

REFERENCES


