(54) Title: A LEVEL CONVERTER PROVIDED WITH SLEW-RATE CONTROLLING MEANS

(57) Abstract: A level converter for the converting of a first digital signal (U₁) having a first voltage range into a second digital signal (U₂) having a second voltage range comprising an amplifier (T₁) having an input for receiving the first digital signal (U₁) and an output for supplying the second digital signal (U₂), a series arrangement for controlling the slew-rate of the second digital signal (U₂) which comprises at least a first capacitor (C₁) and a second capacitor (C₂) and which is coupled between the output and the input of the amplifier (T₁), and voltage controlling means for controlling the voltages (V₁, V₂) across the at least first and second capacitors (C₁, C₂). The voltage controlling means comprises at least one voltage source (V₁, V₂) for supplying a separate bias voltage to each internal node (N₁, N₂) of the series arrangement. The value of the separate bias voltage or the values of the separate bias voltages is / are dependent on the values of the first (U₁) and the second (U₂) digital signals.